Claims

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- 1. Method for generating an information output (seq(wav(cd)))
 to be transmitted over a packet-oriented network (IPNet)
 in which
- 5 a requirement (req(Dst,cd)) for an information output (seq(wav(cd))) is signaled to an information output system (RVS),
 - information (cd) about at least one coding method which can be used for information output is transmitted to the information output system (RVS),
 - for information output a memory system (RCS) with precoded information output components (wav(cd)) is accessed, with the coding method to be used for the information output being notified to the memory system (RCS) by the information output system (RVS),
 - at least one precoded information output component (wav(cd)) precoded with the coding method to be used is transmitted by the memory system (RCS) to the information output system (RVS), and
- 20 an information output (seq(wav(cd))) is formed with the at least one transmitted information output component (wav(cd)).
 - Method in accordance with claim 1, characterized in that
 - the information output comprises audio information..
- 25 3. Method in accordance with claim 2, characterized in that
 - the information output comprises voice information.
 - 4. Method in accordance with claim 1, characterized in that
- 30 the information output comprises video information.
 - 5. Method in accordance with one of the previous claims,

characterized in that,

- to request the information output a standardized signaling protocol is used.
- 6. Method in accordance with claim 5,
- 5 characterized in that

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- the signaling protocol is MGCP or H.248/MEGACO.
- 7. Method in accordance with one of the previous claims, characterized in that,
- creation rules (VXML(Dst)) are transmitted by the memory system (RCS) to the information output system (RVS), and
- the information output (seq(wav(cd))) is formed in accordance with the creation specification (VXML(Dst)) from precoded information output components (wav(cd)).
- Method in accordance with one of the previous claims,
 characterized in that,
 - the precoded information output component (wav(cd)) transmitted by the memory system (RCS) to the information output system (RVS) is stored there for further use.
 - 9. Method in accordance with claim 8, characterized in that,
- 20 the storage is undertaken for a limited time depending on the component to be stored.
 - 10. Method in accordance with one of the previous claims, characterized in that,
- creation specifications (VXML(Dst)) transmitted from the
 25 memory system (RCS) to the information output system (RVS) are
 stored there for further use.
 - 11. Method in accordance with claim 9, characterized in that
- the storage is undertaken for a limited time depending on the creation specification to be stored.

- 12. Method in accordance with one of the previous claims. characterized in that
- precoded information output components (wav(cd)) and creation
 specifications (VXML(Dst)) transmitted from the memory system

 5 (RCS) to the information output system (RVS) are stored in the
 information output system (RVS), and on a request
 (req(Dst,cd)) for an information output (seq(wav(cd))), to form
 the information output (seq(wav(cd))) an information output
 component (wav(cd)) stored in the information output system

 10 (RVS) or a creation specification (VXML(Dst)) stored in the
 information output system is used.
 - 13. Method in accordance with one of the previous claims, characterized in that,
 - an information output design system (ToolRes) is specified,
- precoded information output components (wav(cd)) are generated in the information output design system (ToolRes), and
 - precoded information output components (wav(cd)) generated in the information output design system (ToolRes) are transferred to the memory system (RCS).
 - 14. Method in accordance with one of the previous claims 7 to 13,

characterized in that

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- an information output design system (ToolRes) is specified,
- 25 creation specifications (VXML(Dst)) are generated in the information output design system (ToolRes), and
 - creation specifications (VXML (DST)) generated in the information output design system (ToolRes) are transferred to the memory system (RCS).
- 30 15. Method in accordance with one of the previous claims, characterized in that,
 - precoded information output components (wav(cd)) are

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generated in the memory system (RCS).

- 16. Method in accordance with one of the previous claims, characterized in that,
- creation specifications (VXML(Dst)) are generated in the memory system (RCS).
 - 17. Method in accordance with one of the previous claims, characterized in that,
- the information output (seq(wav(cd))) is also formed with information output components (wav(cd)) generated during the
 processing of the requirements (req(Dst,cd)).
 - 18. Device for generating an information output (seq(wav(cd))) to be transmitted over a packet-oriented network
 - with an information output system (RVS) for forming of information outputs (seq(wav(cd))) by means of precoded information output components (wav(cd)),
 - with a memory system (RCS) for storing precoded information output components (wav(cd)), whereby
 - precoded information output components (wav(cd)) can be transferred via a standardized interface between the information output system (RVS) and the memory system (RCS).
 - 19. Device in accordance with Claim 18, characterized in that
- the device features an information output design system (ToolRes) for creating precoded information output components (wav(cd)), and precoded information output components (wav(cd)) are transmitted via a standardized interface between the information output design system (ToolRes) and the memory system (RCS).
- 20. Device in accordance with claim 18 or 19, characterized in that,
 - in the information output design system (ToolRes) or the

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memory system (RCS) creation specifications (VXML(Dst)) for the formation of information outputs (seq(wav(cd))) can be generated and can be transmitted to the information output system (RVS).

- Device in accordance with one of the claims 18-20,
 with a plurality of information output systems and at least two memory systems, with each information output system having access to at least two memory systems.
 - 22. Device in accordance with one of the claims 18-21,with at least one information output design system which has

access to a plurality of memory systems.